



AP
SFW
MAIL STOP AF
PATENT
8031-1028

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Yoshihiro NONAKA Conf. 5218

Application No. 10/648,256 Group 2811

Filed August 27, 2003 Examiner Ori Nadav

SEMICONDUCTOR INTEGRATED CIRCUIT
HAVING TRANSISTORS FORMED ON AN
INSULATING SUBSTRATE

RESPONSE AFTER FINAL REJECTION

Assistant Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

March 8, 2006

Sir:

This responds to the Official Action mailed December 13, 2005.

Remarks begin on page 2 of this paper.